

## For SSD application

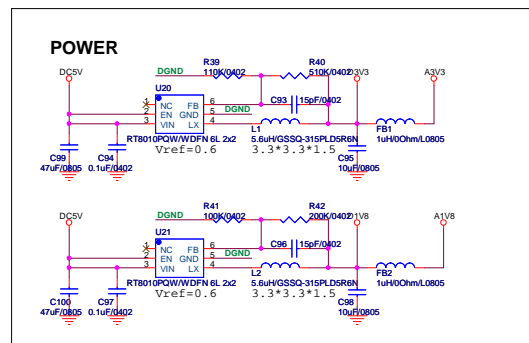
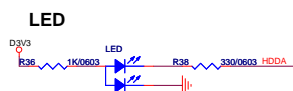
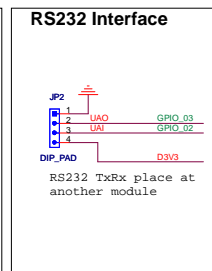
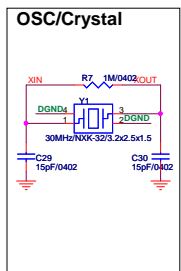
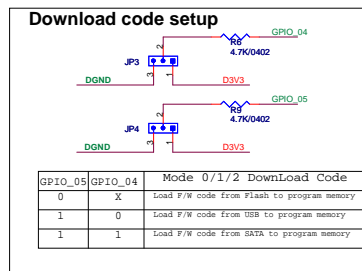
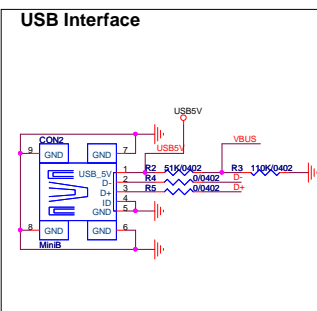
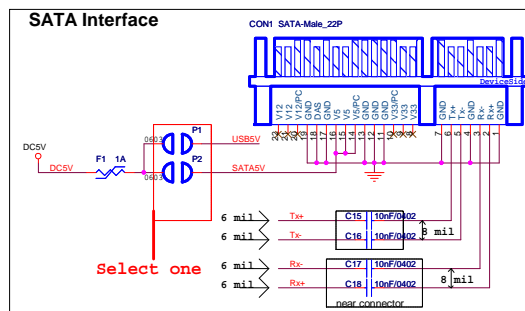
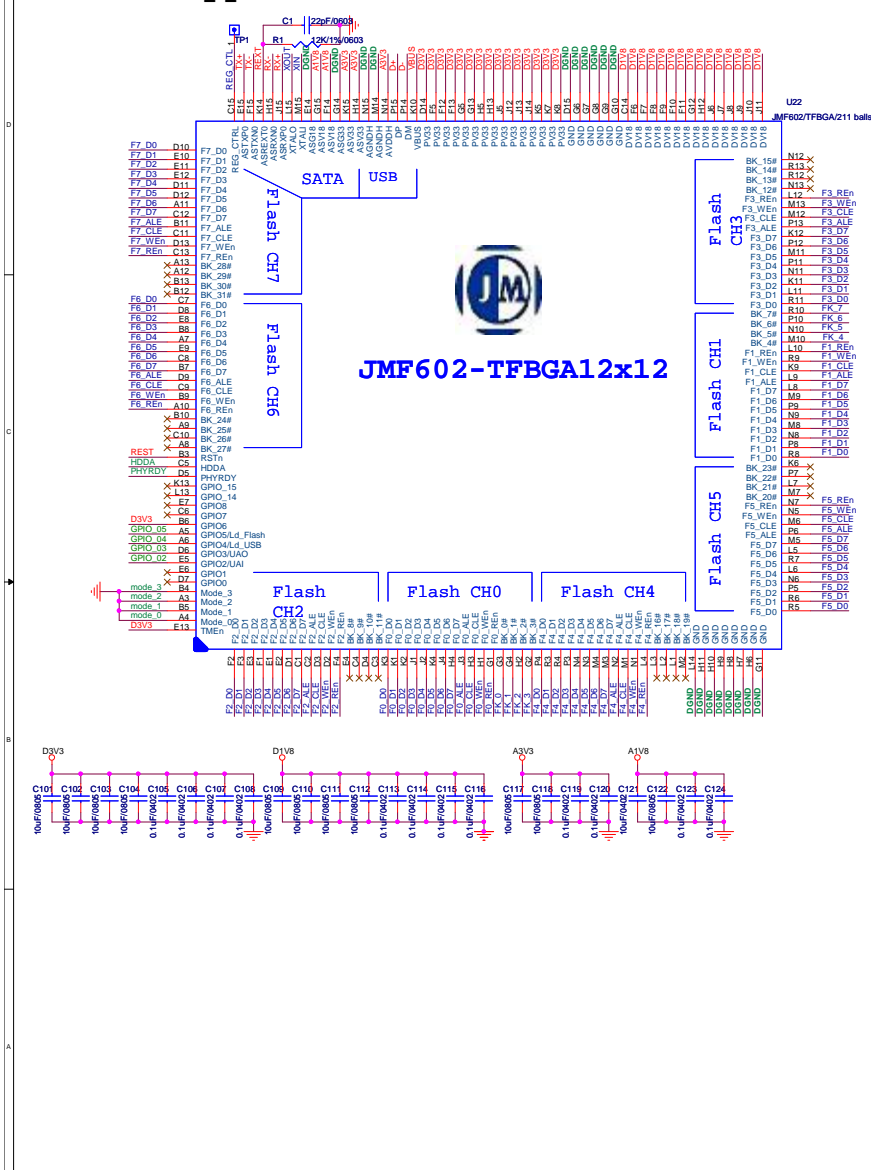


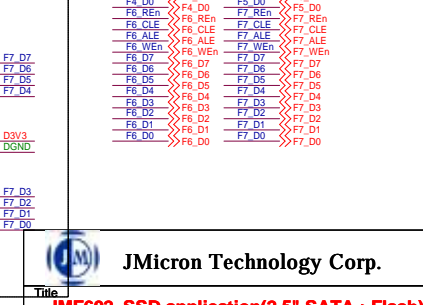
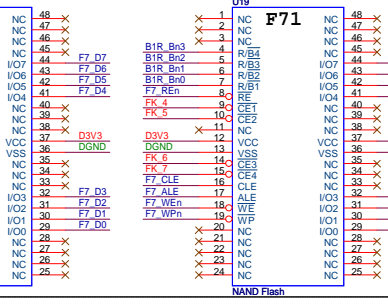
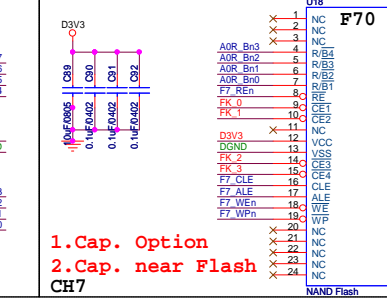
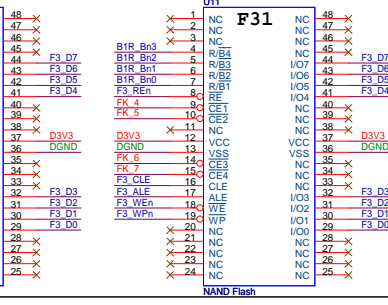
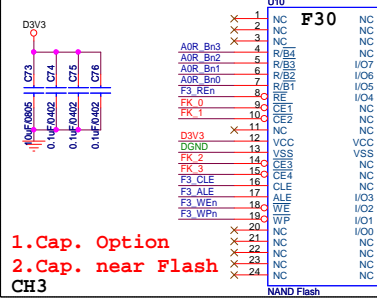
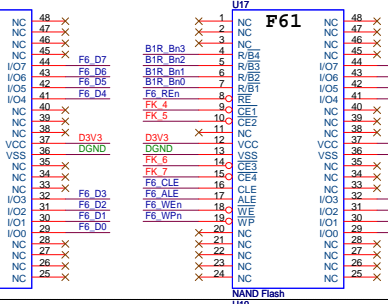
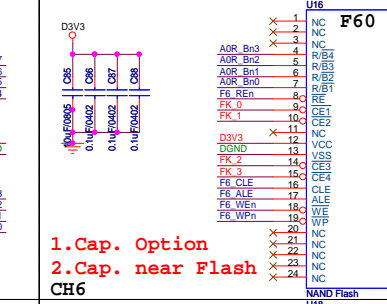
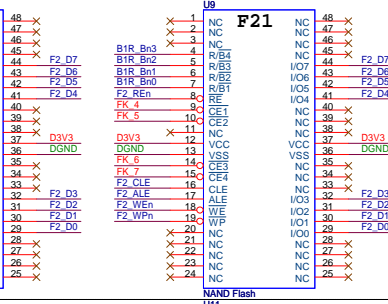
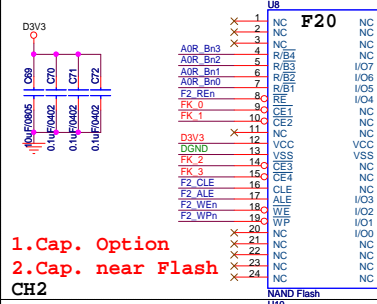
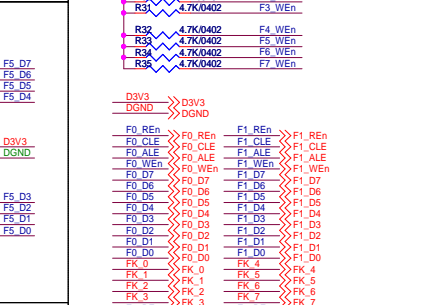
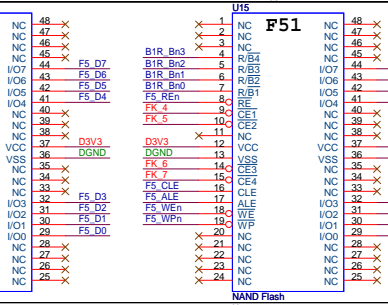
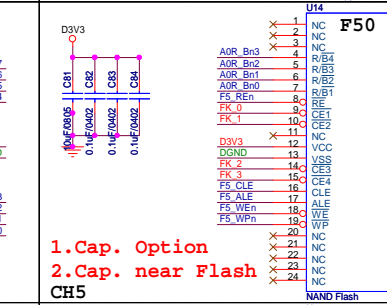
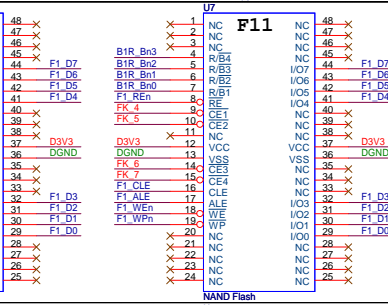
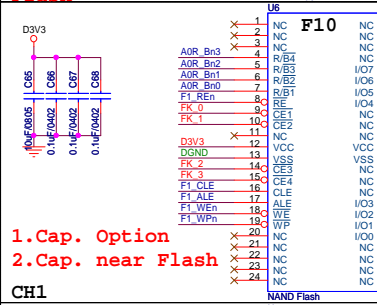
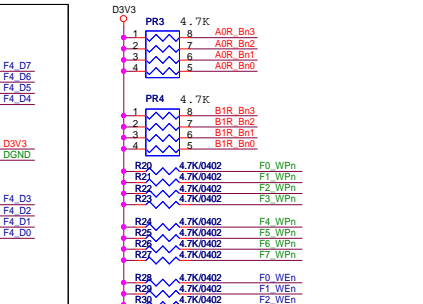
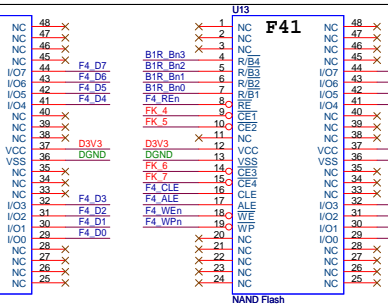
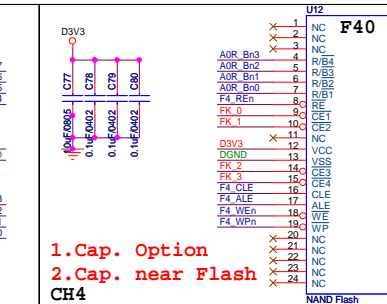
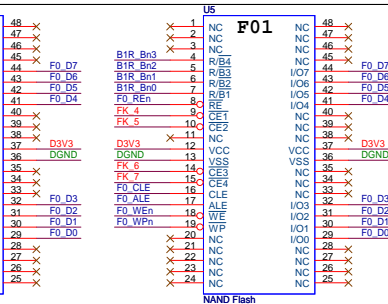
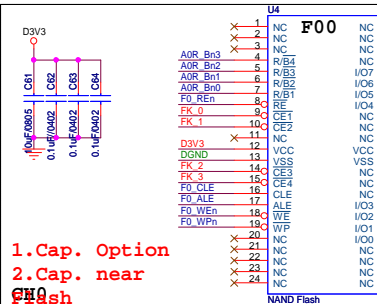
Figure 10 shows the pin connections for the 16-bit parallel data bus. The diagram is divided into two main sections: the top section for the D0-D15 range and the bottom section for the F0-F15 range. Each section contains a table of pin numbers and their corresponding data bus connections.

**Top Section (D0-D15 range):**

Pin	Connection
D0	F0
D1	F1
D2	F2
D3	F3
D4	F4
D5	F5
D6	F6
D7	F7
D8	F8
D9	F9
D10	F10
D11	F11
D12	F12
D13	F13
D14	F14
D15	F15

**Bottom Section (F0-F15 range):**

Pin	Connection
F0	D0
F1	D1
F2	D2
F3	D3
F4	D4
F5	D5
F6	D6
F7	D7
F8	D8
F9	D9
F10	D10
F11	D11
F12	D12
F13	D13
F14	D14
F15	D15



**JMicon Technology Corp.**

Title: **JMF602\_SSD application(2.5" SATA->Flash)**

SCH Number: **JMF602\_DEMO BOARD 2-001-2**

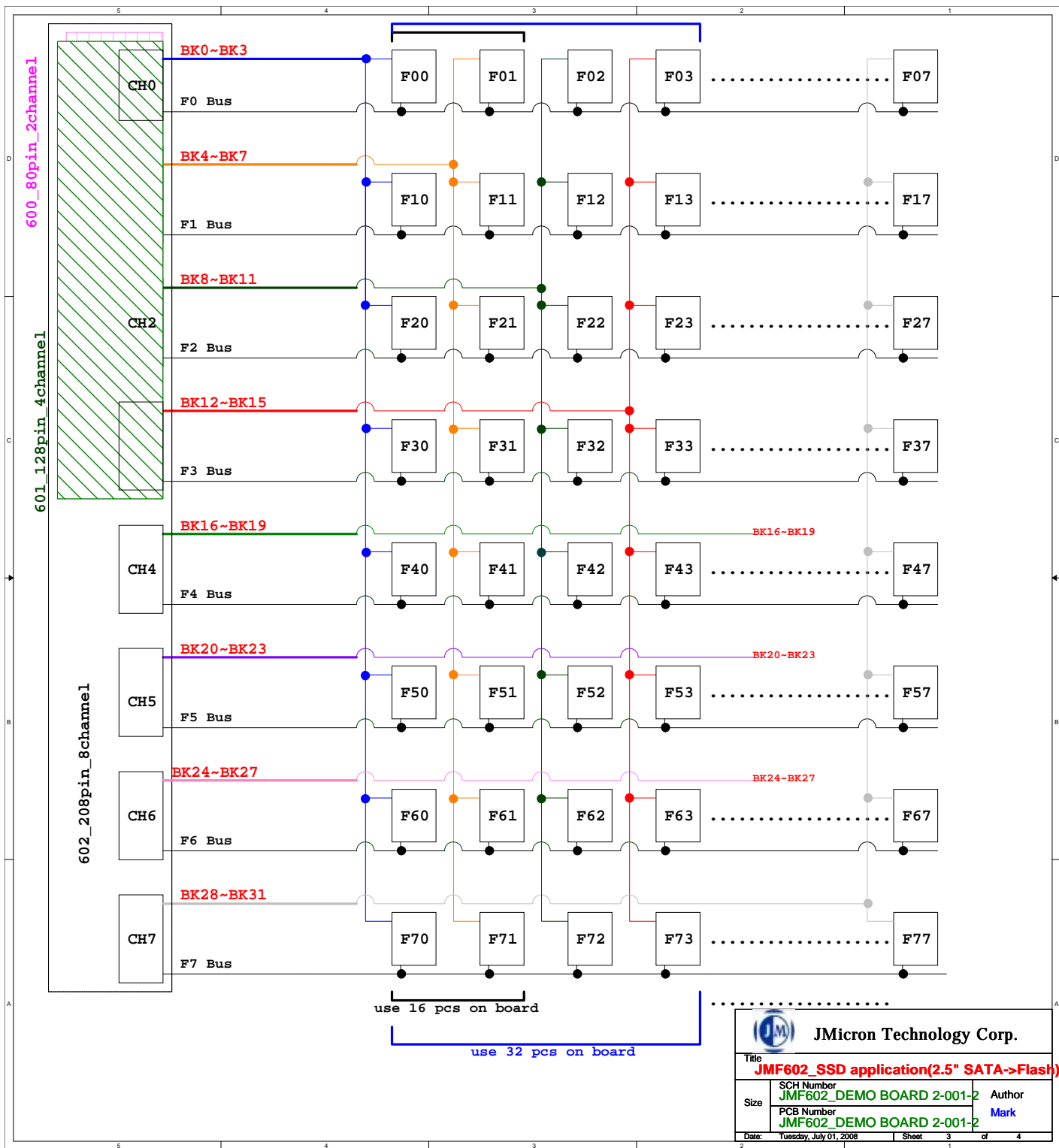
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PCB Number: **JMF602\_DEMO BOARD 2-001-2**

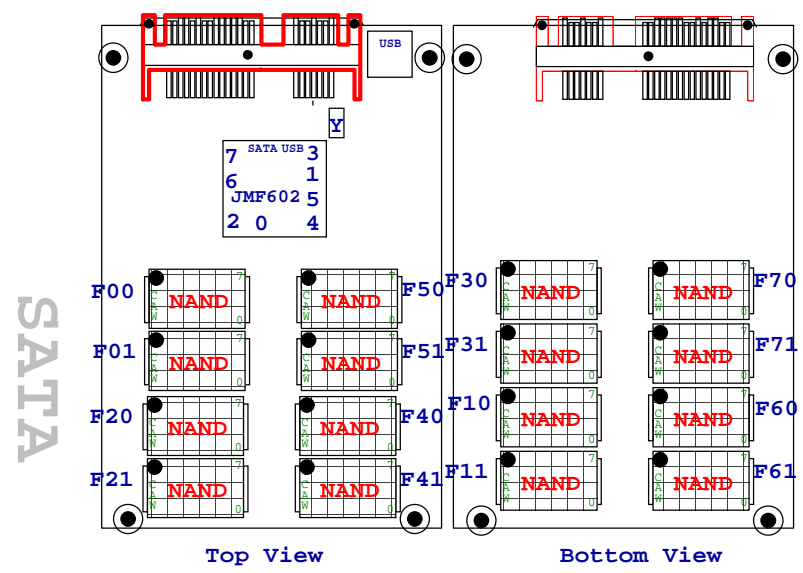
Date: Thursday, July 10, 2008


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Author: **Mark**



Revision Number	Date	Description
02-001-1>02-001-2	08_0118	LQFP->TFBGA
02-001-1>02-001-2	08_0118	Delect C2~C6,C8,C10,C12~C14,C19~C28,C31,C33~C41,C51~C60
02-001-1>02-001-2	08_0118	Add C101~C124



 <b>JMicron Technology Corp.</b>			
<b>Title</b> <b>JMF602_SSD application(2.5" SATA-&gt;Flash)</b>			
<b>Size</b>	<b>SCH Number</b> <b>JMF602_DEMO BOARD 2-001-2</b>	<b>Author</b> <b>Mark</b>	
	<b>PCB Number</b> <b>JMF602_DEMO BOARD 2-001-2</b>		
<b>Date:</b>	<b>Thursday, July 10, 2008</b>	<b>Sheet</b>	<b>4 of 4</b>